L Number	Hits	Search Text	DB	Time stamp
-	1	10/053973	USPAT;	2003/07/06 16:15
			US-PGPUB	
-	1	assembly same validat\$3 same (microelectronic with power	USPAT;	2003/06/09 11:27
		with regulation with system)	US-PGPUB	:
-	1	assembly same validat\$3 same (power with regulation with	USPAT;	2003/06/09 11:27
		system)	US-PGPUB	
-	2	assembly same validat\$3 same (power with regulator)	USPAT;	2003/06/09 11:32
			US-PGPUB	
-	1	(assembly same validat\$3 same (power with regulator)) and	USPAT;	2003/06/09 11:29
		capacitor and substrate	US-PGPUB	2002/06/00 11:25
-	1	assembly and (microelectronic with power with regulat\$4) and substrate and transient and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:35
_	1	assembly and (microelectronic with power with regulat\$4)	USPAT;	2003/06/09 11:35
_	1	and substrate and transient and capacitor	US-PGPUB	2003/00/09 11.33
-	136	assembly and (power with regulat\$4) and substrate and	USPAT;	2003/06/09 11:38
	150	transient and amplitude and capacitor	US-PGPUB	2003/00/03 11.30
-	4	(assembly and (power with regulat\$4) and substrate and	USPAT;	2003/06/09 11:36
		transient and amplitude and capacitor) and (spatial with	US-PGPUB	2003/00/03 11.30
		control with power)		
-	1	assembly and (power adj regulat\$4) and substrate and	USPAT;	2003/06/09 11:39
		(transient adj generator) and amplitude and capacitor	US-PGPUB	
-	1	assembly and (power with regulat\$4) and substrate and	USPAT;	2003/06/09 11:41
		(transient adj generator) and amplitude and capacitor	US-PGPUB	
-	14	assembly and (power with regulat\$4) and substrate and	USPAT;	2003/06/09 11:44
		(transient adj event) and amplitude and capacitor	US-PGPUB	
-	1	assembly and (power adj regulator) and substrate and	USPAT;	2003/06/09 11:57
		(transient adj generator) and capacitor	US-PGPUB	
-	1	assembly and (power adj regulator) and substrate and	USPAT;	2003/06/09 11:43
		(transient adj generator)	US-PGPUB	
-	1	(power adj regulator) and substrate and (transient adj	USPAT;	2003/06/09 11:43
		generator)	US-PGPUB	2002/06/00 44 50
-	4	assembly and (power with regulator) and substrate and	USPAT;	2003/06/09 11:50
	1	(transient adj event) and amplitude and capacitor (assembly and (microelectronic with power with regulat\$4)	US-PGPUB	2002/05/00 11:47
-	1	and substrate) and transient	USPAT; US-PGPUB	2003/06/09 11:47
-	7	assembly and (microelectronic with power with regulat\$4)	USPAT;	2003/06/09 12:06
	,	and substrate	US-PGPUB	
-	4		USPAT;	2003/06/09 11:51
		(transient adj event) and amplitude and capacitor) and	US-PGPUB	
]	(microprocessor or computer or CPU or microcomputer)		
-	1	assembly and (power adj regulator) and substrate and	USPAT;	2003/06/09 11:58
		(transient adj generator)	US-PGPUB	
-	1	substrate same (transient adj generator)	USPAT;	2003/06/09 11:58
			US-PGPUB	
-	31	substrate same (transient with generator)	USPAT;	2003/06/09 12:00
			US-PGPUB	
-	1	(substrate same (transient with generator)) and (power	USPAT;	2003/06/09 12:00
		with regulation with system)	US-PGPUB	
-	33	(power with regulation with system) and (transient with	USPAT;	2003/06/09 12:00
	-	generator)	US-PGPUB	2002/05/02 42 55
-	1	((power with regulation with system) and (transient with	USPAT;	2003/06/09 12:02
		generator)) and substrate	US-PGPUB	2002/05/00 42:02
-	21	((power with regulation with system) and (transient with	USPAT;	2003/06/09 12:03
	5	generator)) and capacitor (((power with regulation with system) and (transient with	US-PGPUB	2003/05/00 12:07
-	5	generator) and capacitor) and assembly	USPAT; US-PGPUB	2003/06/09 12:07
_	1	((((power with regulation with system) and (transient with	USPAT;	2003/06/09 12:07
		generator) and capacitor) and assembly) and surface	US-PGPUB	2003/00/03 12.0/
-	4	("5394025" "5886515" "5942886" "4042830").pn.	USPAT;	2003/07/06 16:17
	'		US-PGPUB	2000,07,00 10.17
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	· · · · · · · · · · · · · · · · · · ·				
-	0	(("5394025" "5886515" "5942886" "4042830").pn.) and	USPAT; US-PGPUB	2003/07/06 16:32	
	53	(testing with power with regulator) (testing with power with regulator)	US-PGPUB USPAT;	2003/07/06 16:18	
-			US-PGPUB	2003/07/00 10.18	
-	1	((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:35	
	-	source) and (control with circuit) and transistor and (drain	US-PGPUB	2000/07/00 10:00	
		with region)			
-	15	((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:22	
		source) and (control with circuit) and transistor	US-PGPUB		
-	1	((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:37	
		source) and transistor and (drain with region)	US-PGPUB		
-	1	(((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:24	
1		source) and (control with circuit) and transistor) and	US-PGPUB		
	2	microelectronic (((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:25	
-	2	source) and (control with circuit) and transistor) and (gate	US-PGPUB	2003/07/00 10.23	
		with region)			
-	2	((((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:24	
		source) and (control with circuit) and transistor) and (gate	US-PGPUB		
		with region)) and (current with source)			
-	3	(((testing with power with regulator)) and (voltage with	USPAT;	2003/07/06 16:26	
		source) and (control with circuit) and transistor) and	US-PGPUB		
		(trigger with signal with input)	LICDAT.	2002/07/06 16:20	
-	2	(("5394025" "5886515" "5942886" "4042830").pn.) and	USPAT; US-PGPUB	2003/07/06 16:30	
	0	(testing with power) (("5394025" "5886515" "5942886" "4042830").pn.) and	USPAT;	2003/07/06 17:30	
-	0	(testing with microelectronic with power)	US-PGPUB	2003/07/00 17.50	
-	8	(testing with microelectronic with power)	USPAT;	2003/07/06 16:33	
			US-PGPUB		
-	1	((testing with microelectronic with power)) and (voltage	USPAT;	2003/07/06 17:17	
		with source) and (control with circuit) and transistor and	US-PGPUB		
		(drain with region)			
-	1	((testing with microelectronic with power)) and (voltage	USPAT;	2003/07/06 16:36	
		with source) and (control with circuit) and transistor and	US-PGPUB		
	1	(current with source) ((testing with microelectronic with power)) and (voltage	USPAT;	2003/07/06 17:32	
-	1	with source) and (control with circuit) and (current with	US-PGPUB	2003/07/00 17.32	
		source)			
-	5	((testing with microelectronic with power)) and (voltage	USPAT;	2003/07/06 17:06	
		with source) and (control with circuit)	US-PGPUB		
-	1	(((testing with microelectronic with power)) and (voltage	USPAT;	2003/07/06 16:42	
		with source) and (control with circuit)) and transistor and	US-PGPUB		
	-	(drain with region)	LICEAT	2002/07/06 16 15	
-	5	(((testing with microelectronic with power)) and (voltage	USPAT;	2003/07/06 16:40	
_	1	with source) and (control with circuit)) and transistor ((testing with microelectronic with power)) and transistor	US-PGPUB USPAT;	2003/07/06 17:17	
-	1	and (drain with region)	US-PGPUB	2003/07/00 17.17	
-	1	((testing with power with regulator)) and transistor and	USPAT;	2003/07/06 16:42	
	_	(drain with region)	US-PGPUB		
-	26821	transistor with (drain with region)	USPAT;	2003/07/06 16:45	
			US-PGPUB		
-	1	(transistor with (drain with region)) and ((testing with	USPAT;	2003/07/06 16:44	
		power with regulator))	US-PGPUB		
-	1	(transistor with (drain with region)) and (transient with load	USPAT;	2003/07/06 16:45	
	4400	with generator)	US-PGPUB	2002/07/06 16.47	
-	4400	(voltage with source) with transistor with (drain with region)	USPAT; US-PGPUB	2003/07/06 16:47	
_	1579	((voltage with source) with transistor with (drain with	USPAT;	2003/07/06 16:49	
	15,5	region)) and (voltage with source) and (control with circuit)	US-PGPUB	2000/07/00 10.49	
-	2	((voltage with source) with transistor with (drain with	USPAT;	2003/07/06 16:49	
		region)) and (voltage with source) and (control with	US-PGPUB		
		circuit)) and trnsistor and (gate with region)			
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·			, _	
-	0	(((("5394025" "5886515" "5942886" "4042830").pn.) and	USPAT;	2003/07/06 17:17
		(testing with power)) and (voltage with source) and	US-PGPUB	
	~	(control with circuit) and transistor) and (drain with region)	LICDAT	2002/07/26 45 10
-	2	((("5394025" "5886515" "5942886" "4042830").pn.) and	USPAT;	2003/07/06 17:18
		(testing with power)) and (voltage with source) and	US-PGPUB	
	E10	(control with circuit) and transistor	LICDAT.	2002/07/06 17.52
-	512	(testing with (microprocessor or CPU or computer or	USPAT;	2003/07/06 17:52
	68	microcomputer) with power)	US-PGPUB	2002/07/06 17.27
-	60	((testing with (microprocessor or CPU or computer or	USPAT; US-PGPUB	2003/07/06 17:37
		microcomputer) with power)) and (voltage with source) and	US-PGPUB	
	2	(control with circuit) and (current with source)	LICDAT	2003/07/06 17:34
-	2	(((testing with (microprocessor or CPU or computer or microcomputer) with power)) and (voltage with source) and	USPAT; US-PGPUB	2003/07/08 17:34
		(control with circuit) and (current with source)) and	03-PGP0D	
		transistor and (gate with region) and (drain with region)		
_	2	((testing adj5 (microprocessor or CPU or computer or	USPAT;	2003/07/06 17:37
	2	microcomputer) adj2 power)) and (voltage with source) and	US-PGPUB	2003/07/00 17.37
		(control with circuit) and (current with source)	03-FGF00	
_	16	(testing adj5 (microprocessor or CPU or computer or	USPAT;	2003/07/06 17:45
	10	microcomputer) adj2 power)	US-PGPUB	
_	2	(testing with (microprocessor or CPU or computer or	USPAT;	2003/07/07 16:24
	-	microcomputer) with power) and (transient with load with	US-PGPUB	
		generator)		
-	1	(testing with (microprocessor or CPU or computer or	USPAT;	2003/07/08 13:02
	_	microcomputer) with power) and (transient with load with	US-PGPUB	
		generator) and (voltage with source) and (control with		
		circuit)		
-	27	(transient with load with generator) and (first with voltage	USPAT;	2003/07/07 16:52
		with source) and (second with voltage with source) and	US-PGPUB	
		(control with circuit)		
-	3	((transient with load with generator) and (first with voltage	USPAT;	2003/07/07 16:37
		with source) and (second with voltage with source) and	US-PGPUB	
		(control with circuit)) and drain and gate and transistor		
-	1	(transient with load with generator) and (voltage with	USPAT;	2003/07/07 17:17
		source) and (control with circuit) and (drain with transistor	US-PGPUB	
		with voltage with source) and (gate with region with		
		transistor with control with circuit) and transistor		
-	1	(transient with load with generator) and (voltage with	USPAT;	2003/07/07 17:02
		source) and (control with circuit) and (drain with transistor)	US-PGPUB	
		and (gate with region with transistor) and transistor		2002/07/07 47:42
-	1	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:13
		307/\$.ccls.) and (transient with load with generator) and	US-PGPUB	
		(voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and		
		transistor		
_	929	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:34
	323	307/\$.ccls.) and (voltage with source) and (control with	US-PGPUB	2003/07/07 17.34
		circuit) and (drain with transistor) and (gate with region with		
		transistor) and transistor		
-	334		USPAT:	2003/07/07 17:19
	551			
-	8	(((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:20
	_		US-PGPUB	,,
		circuit) and (drain with transistor) and (gate with region with		
		region with transistor) and (voltage with gate with region		
		with transistor) and transistor) and (transient with load)		1
-	334 8	307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and transistor) and (voltage with drain with		

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-	1	(((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:49
		307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with	US-PGPUB	
		transistor) and transistor) and (control with circuit with drain		
		with region with transistor) and (voltage with gate with		
		region with transistor) and transistor) and (transient with		
		load)		
-	1	(((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:22
	-	307/\$.ccls.) and (voltage with source) and (control with	US-PGPUB	2003/07/07 17.22
		circuit) and (drain with transistor) and (gate with region with	0010100	
	1	transistor) and transistor) and (control with circuit with drain		
		with region with transistor) and (voltage with gate with		
		region with transistor) and transistor) and (testing with		
		(microprocessor or CPU or computer or microcomputer) with		
:		power)		
_	38	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:21
		307/\$.ccis.) and (voltage with source) and (control with	US-PGPUB	
		circuit) and (drain with transistor) and (gate with region with		
		transistor) and transistor) and (control with circuit with drain		
		with region with transistor) and (voltage with gate with		
]		region with transistor) and transistor		
] -	2	(((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:22
1		307/\$.ccls.) and (voltage with source) and (control with	US-PGPUB	
		circuit) and (drain with transistor) and (gate with region with		
		transistor) and transistor) and (control with circuit with drain		
		with region with transistor) and (voltage with gate with		
		region with transistor) and transistor) and ((microprocessor		
		or CPU or computer or microcomputer) with power)		2002/07/07 47 20
-	303	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:38
		307/\$.ccls.) and (voltage with source) and (control with	US-PGPUB	
		circuit) and (current with source) and (control with circuit		
	13	with trigger with signal) and switch and transistor	USPAT;	2003/07/08 10:02
-	12	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with	US-PGPUB	2003/07/08 10.02
		circuit) and (current with source) and (control with circuit	03-FGF0D	
		with trigger with signal) and switch and transistor) and		
		(transistor with voltage with source) and ((current adj		
		source) with (voltage adj source))		
-	1	(((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/07 17:49
	_	307/\$.ccls.) and (voltage with source) and (control with	US-PGPUB	
		circuit) and (current with source) and (control with circuit		
		with trigger with signal) and switch and transistor) and		
	1	(transistor with voltage with source) and ((current adj	-	
	1	source) with (voltage adj source))) and (transient with load)		
-	316	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/08 10:29
	1	307/\$.ccls.) and (voltage with source) and (current with	US-PGPUB	
	1	source) and (transistor with voltage with source) and		
		((current adj source) with (voltage adj source)) and		
		(transistor with ground with (current adj source))		2002/02/00 44:22
-	16	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 327/	USPAT;	2003/07/08 11:28
	ł	307/\$.ccls.) and (voltage with source) and (current with	US-PGPUB	
1	ł	source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and		
	ł	((current ad) source) with (voltage ad) source)) and (transistor with ground with (current adj source))) and		
		(test\$3 with power)		
-	316	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/08 10:32
		307/\$.ccls.) and (voltage adj2 source) and (current adj2	US-PGPUB	
		source) and (transistor with voltage with source) and		
		((current adj source) with (voltage adj source)) and		
		(transistor with ground with (current adj source))		
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-	291	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/08 10:33
		307/\$.ccls.) and (voltage adj2 source) and (current adj2	US-PGPUB	
		source) and (transistor with (voltage adj2 source)) and		
2		((current adj source) with (voltage adj source)) and		
		(transistor with ground with (current adj source))		
-	14	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or	USPAT;	2003/07/08 11:22
		307/\$.ccls.) and (voltage adj2 source) and (current adj2	US-PGPUB	
		source) and (transistor with (voltage adj2 source)) and		
		((current adj source) with (voltage adj source)) and		
		(transistor with ground with (current adj source))) and		
	1001	(test\$3 with power)		2002/07/00 44.27
-	1291	((current adj2 source) with (couple\$2 or connect\$2) with	USPAT;	2003/07/08 11:27
		(voltage adj2 source)) and (transistor with (couple\$2 or	US-PGPUB	
		connect\$2) with (current adj2 source)) and (transistor with		
	50	ground)		2002/07/09 11:29
-	56	(((current adj2 source) with (couple\$2 or connect\$2) with	USPAT; US-PGPUB	2003/07/08 11:28
		(voltage adj2 source)) and (transistor with (couple\$2 or	03-PGP00	
		connect\$2) with (current adj2 source)) and (transistor with		
	8	ground)) and (test\$3 with power) 702/\$.ccls. and (testing with (microprocessor or CPU or	USPAT;	2003/07/08 13:05
-	0	computer or microcomputer) with power) and (voltage with	US-PGPUB	2003/07/08 13.05
		source) and (control with circuit)	03-10-00	
-	3	702/\$.ccls. and (testing near5 (microprocessor or CPU or	USPAT;	2003/07/08 13:10
		computer or microcomputer or microelectronic) near5	US-PGPUB	
		power) and (voltage with source) and (control with circuit)	0010100	
-	17	702/\$.ccls. and (testing near3 power) and (voltage with	USPAT;	2003/07/08 13:11
		source) and (control with circuit)	US-PGPUB	
-	4452	(voltage near2 source) and (control\$4 with circuit) and	USPAT;	2004/03/18 17:01
		transistor and (drain near2 region) and (source near2	US-PGPUB;	
		region)	EPO; JPO;	
			DERWENT	
-	10	((voltage near2 source) and (control\$4 with circuit) and	USPAT;	2004/03/18 18:19
		transistor and (drain near2 region) and (source near2	US-PGPUB;	
		region)) and (power near5 delivery)	EPO; JPO;	
			DERWENT	
-	4	(voltage near2 source) and (control\$4 with circuit) and	USPAT;	2004/03/18 18:25
		transistor and ((drain or collector) near2 region) and	US-PGPUB;	
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (power near5 delivery)	DERWENT	
-	3	(voltage near2 source) and (control\$4 with circuit) and	USPAT;	2004/03/18 18:27
		transistor and ((drain or collector) near2 region) and	US-PGPUB;	1
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (power near5 delivery) and (current near2	DERWENT	
		Source)	LICDAT	2004/03/18 18:52
-	82	(first near2 voltage near2 source) and (second near2 voltage near2 source) and (control\$4 with circuit) and transistor	USPAT; US-PGPUB;	2004/05/10 10:52
	1		EPO; JPO;	
		and ((drain or collector) near2 region) and ((source or emitter) near2 region) and ((gate or base) near2 region)	DERWENT	
		and (current near2 source)		
	2	6281724.pn.	USPAT;	2004/03/19 16:49
-	2	l correr.hu	US-PGPUB;	
			EPO; JPO;	
			DERWENT	
-	2	("4812672" "5187396").PN.	USPAT	2004/03/18 18:50
	53	(first near2 voltage near2 source) and (second near2 voltage	USPAT;	2004/03/19 14:45
		near2 source) and (control\$4 near2 circuit) and transistor	US-PGPUB;	
		and ((drain or collector) near2 region) and ((source or	EPO; JPO;	
		emitter) near2 region) and ((gate or base) near2 region)	DERWENT	
		and (current near2 source)		
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-	31	(first near2 voltage near2 source) and (second near2 voltage	USPAT;	2004/03/19 09:42
		near2 source) and (control\$4 near2 circuit) and transistor	US-PGPUB;	
		and ((drain or collector) near2 region) and ((source or	EPO; JPO;	
		emitter) near2 region) and ((gate or base) near2 region)	DERWENT	
		and (current near2 source) and (output near2 voltage)		
-	333	(voltage adj source) and (control\$4 near2 circuit) and	USPAT;	2004/03/19 09:43
•		transistor and ((drain or collector) near2 region) and	US-PGPUB;	
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (current near2 source) and (output near2	DERWENT	
		voltage)		
-	249	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 09:45
		transistor and ((drain or collector) near2 region) and	US-PGPUB;	, ,
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (current near2 source) and (output near2	DERWENT	
		voltage)	DENTEIN	
_	3	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 09:45
		transistor and ((drain or collector) near2 region) and	US-PGPUB;	2007/03/13 03.43
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (current near2 source) and (output near2	DERWENT	
	140	voltage)) and (transient with load)	LICDAT	2004/02/40 00.50
-	446	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 09:50
		transistor and ((drain or collector) near2 region) and	US-PGPUB;	
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (output near2 voltage)	DERWENT	
-	4	((voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 09:59
		transistor and ((drain or collector) near2 region) and	US-PGPUB;	
		((source or emitter) near2 region) and ((gate or base) near2	epo; jpo;	
		region) and (output near2 voltage)) and (transient with	DERWENT	
		load)		
-	16	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 09:57
		transistor and (transient with load with generator) and	US-PGPUB;	
		(output near2 voltage)	EPO; JPO;	
			DERWENT	
-	1	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 09:59
		transistor and (transient with load with generator) and	US-PGPUB;	
		(gate with region) and (drain with region) and (source with	EPO; JPO;	
		region)	DERWENT	
-	1328	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 10:13
		transistor and (gate with region) and (drain with region) and	US-PGPUB;	
		(source with region)	EPO; JPO;	
		· · · · · · · · · · · · · · · · · · ·	DERWENT	
-	9	((voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 10:07
		transistor and (gate with region) and (drain with region) and	US-PGPUB;	
		(source with region)) and (transient with load)	EPO; JPO;	
			DERWENT	
_	10	((voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 10:08
	10	transistor and (gate with region) and (drain with region) and	US-PGPUB;	2007/03/13 10:00
		(source with region)) and (power with delivery)	EPO; JPO;	
		(Source mutregion) and (power with delivery)	DERWENT	
	234	(voltage adj course) and (controlt4 adj size it) and		2004/02/10 12:25
-	234	(voltage adj source) and (control\$4 adj circuit) and	USPAT;	2004/03/19 12:25
		transistor and (gate with region) and (drain with region) and	US-PGPUB;	
		(source with region) and (second near5 output near5	EPO; JPO;	
		voltage) and (first near5 output near5 voltage)	DERWENT	2004/02/10 12 13
-	32	(output with voltage with coupled with drain with region	USPAT;	2004/03/19 12:44
		with transistor) and (output with voltage with coupled with	US-PGPUB;	
		source with region with transistor) and (control adj circuit)	EPO; JPO;	
			DERWENT	
-	32	(output with voltage with coupled with drain with source	USPAT;	2004/03/19 14:36
		with region with transistor) and (control adj circuit)	US-PGPUB;	
			EPO; JPO;	
			DERWENT	
	•			·

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-	12	(output with voltage with coupled with (drain adj region) with (source adj region) with transistor) and (control adj	USPAT; US-PGPUB;	2004/03/19 14:37
		circuit)	EPO; JPO;	
	10		DERWENT	000000000000000
-	10	(first adj voltage adj source) and (second adj voltage adj	USPAT;	2004/03/19 16:07
		source) and (control\$4 near2 circuit) and transistor and	US-PGPUB;	
		((drain or collector) near2 region) and ((source or emitter)	EPO; JPO;	
		near2 region) and ((gate or base) near2 region) and	DERWENT	
		(current near2 source)		2004/02/40 45 50
-	27	(first adj voltage adj source) and (second adj voltage adj	USPAT;	2004/03/19 15:58
		source) and (control\$4 near2 circuit) and transistor and	US-PGPUB;	
		((drain or collector) near2 region) and ((source or emitter)	EPO; JPO;	
	167	near2 region) and ((gate or base) near2 region)	DERWENT	2004/02/10 16:06
-	167	327/\$.ccls. and (voltage adj source) and (control\$4 near2	USPAT;	2004/03/19 16:06
		circuit) and transistor and ((drain or collector) near2 region)	US-PGPUB;	
		and ((source or emitter) near2 region) and ((gate or base)	EPO; JPO;	
		near2 region)	DERWENT	2004/02/10 16:00
-	4	(327/\$.ccls. and (voltage adj source) and (control\$4 near2	USPAT;	2004/03/19 16:00
		circuit) and transistor and ((drain or collector) near2 region)	US-PGPUB;	
		and ((source or emitter) near2 region) and ((gate or base)	EPO; JPO;	
	-	near2 region)) and (transient with load)		2004/02/10 16:21
-	1	(first adj voltage adj source) and (second adj voltage adj source) and (control\$4 near2 circuit) and transistor and	USPAT; US-PGPUB;	2004/03/19 16:21
		((drain or collector) near2 region) and ((source or emitter)		
			EPO; JPO; DERWENT	
		near2 region) and ((gate or base) near2 region) and	DERVVENT	
	18	(transient with load) (voltage adj source) and (control\$4 near2 circuit) and	USPAT;	2004/03/19 16:20
	10	transistor and ((drain or collector) near2 region) and	US-PGPUB;	
		((source or emitter) near2 region) and ((gate or base) near2	EPO; JPO;	
		region) and (transient with load)	DERWENT	
	17	(first adj voltage adj source) and (second adj voltage adj	USPAT;	2004/03/19 16:37
		source) and (control\$4 near2 circuit) and transistor and	US-PGPUB;	2001/03/19 10:57
		(transient with load)	EPO; JPO;	
1			DERWENT	
-	17	((first adj voltage adj source) and (second adj voltage adj	USPAT;	2004/03/19 17:46
•		source) and (control\$4 near2 circuit) and transistor and	US-PGPUB;	
		(transient with load)) and (voltage adj source)	EPO; JPO;	
			DERWENT	
-	0	6281724.pn. and (power with regulation)	USPAT;	2004/03/19 17:14
		· · · · · · · · · · · · · · · · · · ·	US-PGPUB;	, ,
]		EPO; JPO;	
	1		DERWENT	
-	0	6281724.pn. and (transient with load with generator)	USPAT;	2004/03/19 17:15
			US-PGPUB;	
			EPO; JPO;	
	1		DERWENT	
-	0	6281724.pn. and (transient with load)	USPAT;	2004/03/19 17:15
	1		US-PGPUB;	
	1		EPO; JPO;	
]		DERWENT	
-	8	((first adj voltage adj source) and (second adj voltage adj	USPAT;	2004/03/19 17:47
		source) and (control\$4 near2 circuit) and transistor and	US-PGPUB;	
		(transient with load)) and (voltage adj source) and (drain	EPO; JPO;	
L		with source)	DERWENT	